

**Remarks/Arguments**

Claims 1, 3-42, 44-50 and 52 are pending in the above-captioned application. Claims 2, 43, 51 and 53 have been cancelled without prejudice herein. Independent claims 1, 27, 50 and 52 have been amended herein to disclose that the claimed device of those claims includes one or more nanowire transistors each of which comprises at least two (or more) nanowires which span between a source and a drain electrode of the transistor. There is replete support throughout the specification and originally filed claims for these claim amendments and thus no new matter is entered.

**I. Objections to the Drawings**

Figures 1 and 2 were objected to because the Examiner indicated that these figures should be designated with the legend "Prior Art." Applicants have complied with the Examiner's request and have submitted herewith two Replacement Sheets for Figures 1 and 2 each bearing the legend "Prior Art." No new matter has been entered.

**II. Rejection Under 35 U.S.C. §103(a)**

Claims 1-53 were rejected under 35 U.S.C. §103(a) as allegedly being unpaetntable over Applicants' admitted prior art in view of Koyama et al. (U.S. Pub. 20020024489) ("Koyama") and Lieber (WO 02/48701) ("Lieber"). To the extent that the Examiner's rejection applies to the amended claims herein, Applicants respectfully traverse those rejections.

Independent claims 1, 27, 50 and 52 have been amended herein to disclose that the claimed device of those claims includes one or more nanowire transistors each of which comprises at least two (or more) nanowires which span between a source and a drain electrode of the transistor. Koyama does not disclose nanowire transistors at all. To the extent that Lieber discloses nanowire transistors, Lieber does not disclose a nanowire transistor having at least two or more nanowires which form a conducting channel between a source and drain contact of the transistor. Although Lieber generally

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**Amendments to the Drawings:**

Figures 1 and 2 have been amended to insert the legend "Prior Art" as suggested by the Examiner. No new matter is entered.

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discloses a nanowire sensor in connection with Figure 16a and the specification at page 30, line 21 through page 31, line 20, for example, Lieber admits that such nanowire sensor only comprises a single conducting element: "Fig. 16a depicts one embodiment of a nanowire sensor. As show in Fig. 16A, the nanowire sensor of the invention comprises a single molecule of doped silicon 50." Page 30, lines 21-22 (emphasis added).

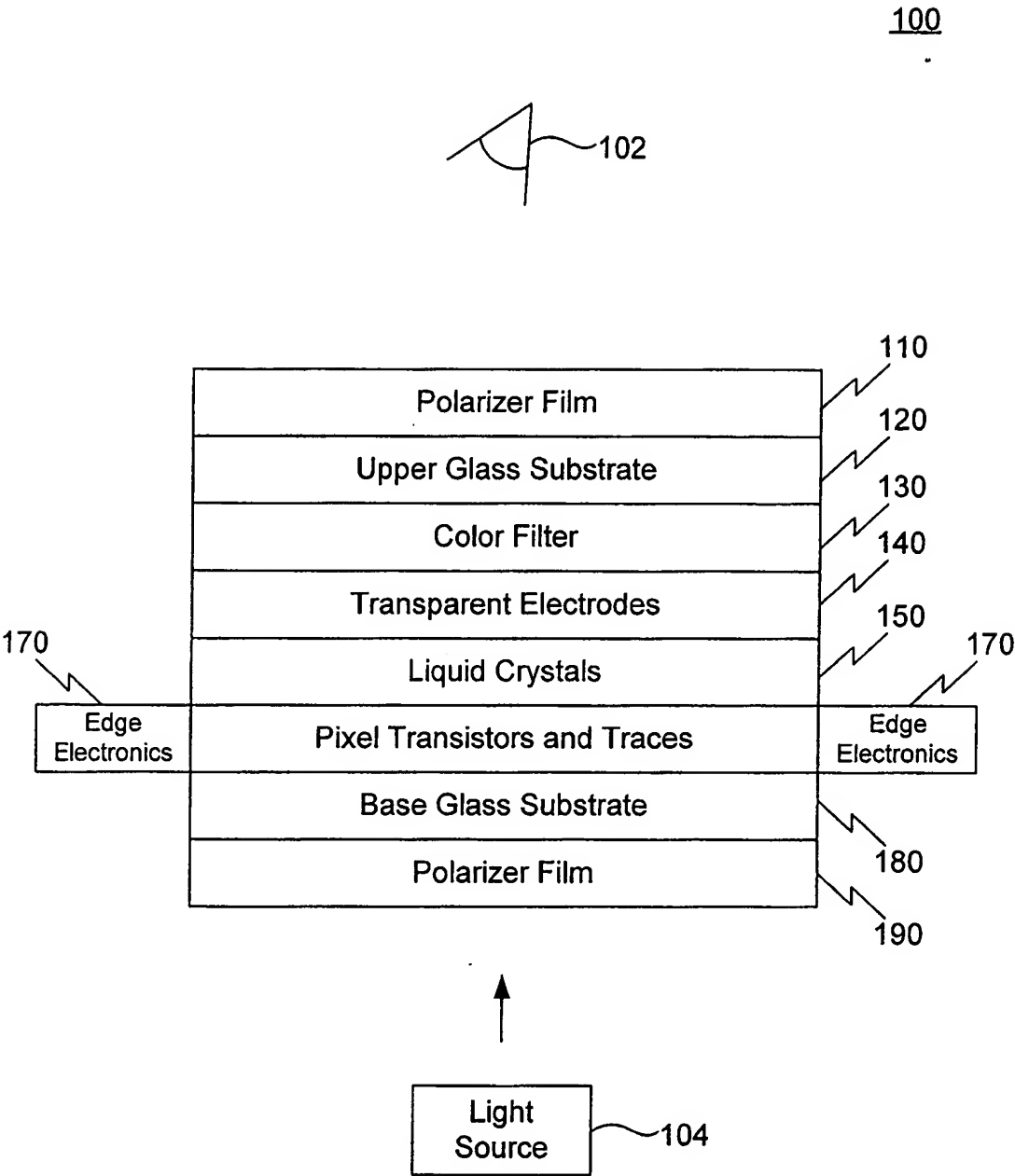
Applicants have shown with the present claimed invention the unique performance advantages that can be achieved in display technology using nanowire transistors with multiple nanowires extending between a source and drain contact of the transistor. See, e.g., specification at paragraphs [0049] and [0055], for example. It would not be obvious to combine Koyama with Lieber to arrive at Applicants' claimed invention, because such a combination does not teach or suggest a device (e.g., a liquid crystal display or active matrix backplane used within a display) comprising one or more nanowire transistors each of which comprises at least two or more nanowires extending between a source contact and a drain contact of the transistor. Accordingly, it is believed that independent claims 1, 27, 50, and 52 are patentable in view of Koyama and Lieber, and withdrawal of the rejection of these claims is respectfully requested. Dependent claims 3-26, 28-42, and 44-49 each depend, directly or indirectly, from an allowable independent claim, and are believed to be allowable for at least these same reasons.

Respectfully submitted,



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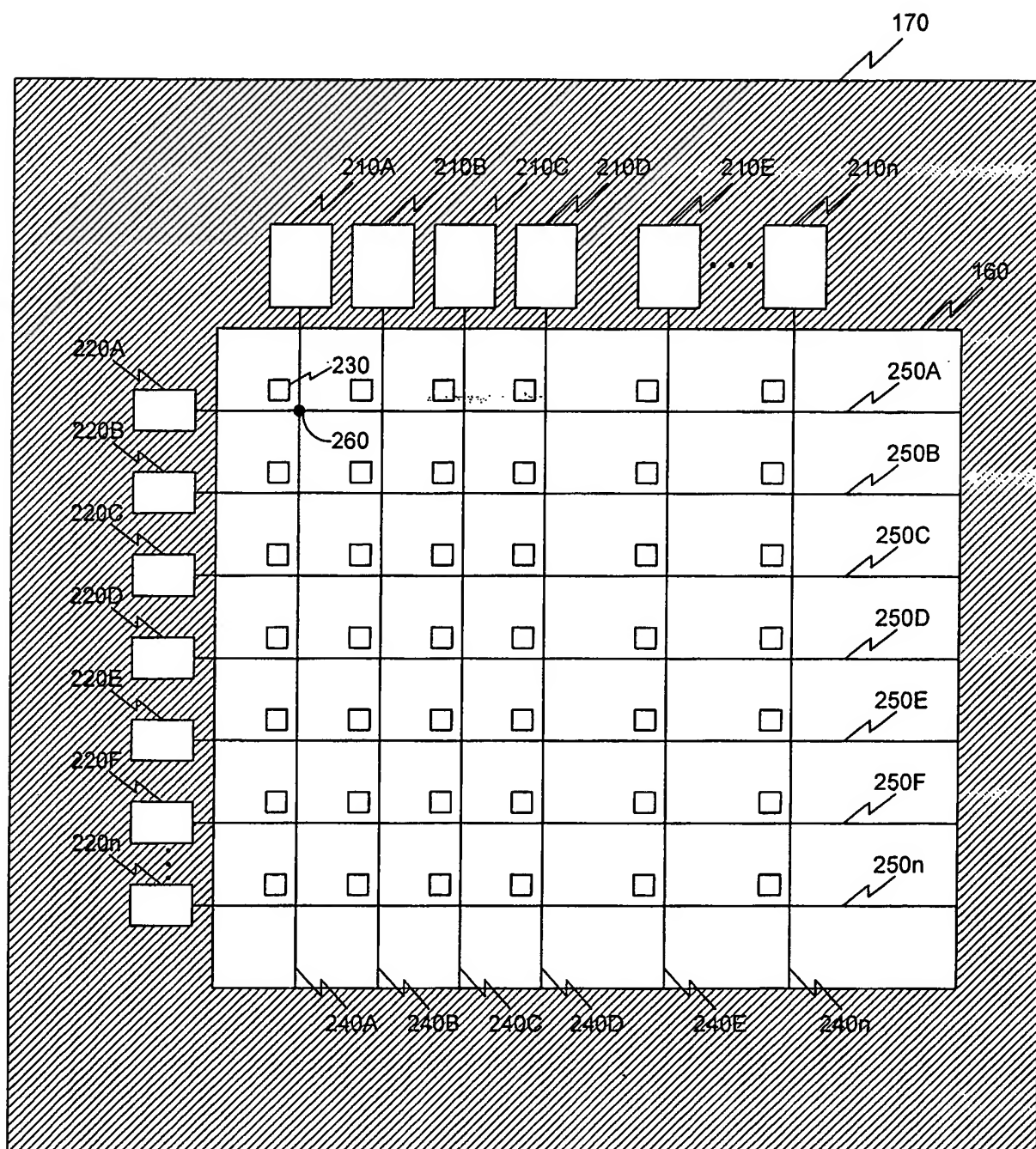
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(Prior Art)

FIG. 1

## Replacement Sheet



(Prior Art)

**FIG. 2**